

Lirida Naviner

Full Professor at Telecom Paris – Institut Polytechnique de Paris (IP Paris)
Department Communications and Electronics (COMELEC)
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30 YEARS OF PROFESSIONAL EXPERIENCE

- Undergraduate, graduate, and continuing education (Universities and *Grandes Écoles*)
- Research & innovation (industrial and academic partnerships, international collaborations)
- Leadership of industrial and academic research projects
- Member of scientific committees (conferences, journals, business incubators, funding agencies)
- Author of 300+ scientific works (papers, patents, etc.)
- Supervision of 30+ PhD (6 ongoing) and numerous Master's theses

ACADEMIC POSITIONS

- Télécom Paris – Institut Polytechnique de Paris, France : Full Professor (since 2009)
- Télécom Paris, France : Associate Professor (1998–2009)
- Universidade Federal da Paraíba, Brazil : Teaching-Research Associate (1994–1997)

RESEARCH TOPICS

- Reliability and Fault-Tolerance of Nanoelectronic Systems
- Security-Aware Computing Architectures
- AI for Hardware Design and Hardware Design for AI
- Spintronic Devices and Memories for Secure, Reliable, and Energy-Efficient Computing

TEACHING (SELECTED)

- Algorithms and Architectures for Digital Computing (Master)
- Security and Reliability of Embedded Systems (Master)
- Advanced R&D Project (Engineering and Master)

CURRENT ACADEMIC RESPONSIBILITIES

- Member of the IP Paris Doctoral School Executive Board
- Member of the IP Paris Department of Information, Communication and Electronics Executive Board
- Member of the Scientific Council of LTCI
- Head of the Secure and Safe Hardware (SSH) Team, Télécom Paris
- Scientific Coordinator of the PhD Program Information, Communication and Electronics
- Academic Coordinator of the Master Program Systèmes Embarqués et Traitement de l'Information

PAST ACADEMIC RESPONSIBILITIES (SELECTED)

- Representative of the College of Full Professors at the LTCI Laboratory (2021–2025)
- Quality Officer with the Education Directorate (2021–2023)
- Academic Coordinator of the Specialized Master Programs at Télécom Paris (2019–2023)

EDUCATION

- Habilitation à Diriger des Recherches, Université Paris VI, France (2006)
- PhD in Electronics and Communications, ENST, France (1994)
- MSc in Microelectronics and Embedded Computing, Université Paris VI, France (1991)
- MSc in Information Processing, UFPB, Brazil (1990)
- BSc in Electrical Engineering, UFPB, Brazil (1988)

DISTINCTIONS

- Officer of the Order of Academic Palms (since 2021)
- IEEE Senior Member (since 2007)